Matter No.: 14225-048001 Page 1 of 6 Applicant(s): Kiyoshi Mita METHOD FOR MANUFACTURING MOUNTING SUBSTRATE AND METHOD FOR MANUFACTURING CIRCUIT DEVICE

FIG.1A

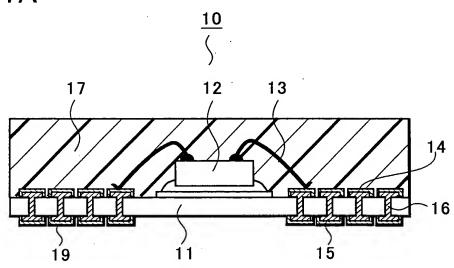
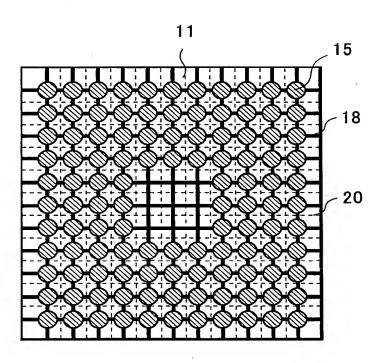


FIG.1B



Matter No.: 14225-048001 Page 2 of 6
Applicant(s): Kiyoshi Mita
METHOD FOR MANUFACTURING MOUNTING SUBSTRATE
AND METHOD FOR MANUFACTURING CIRCUIT DEVICE

FIG.2A

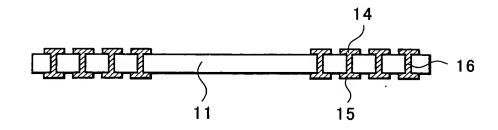
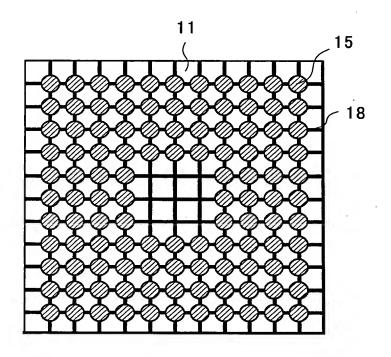
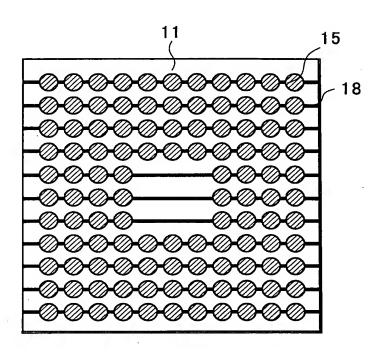


FIG.2B



Applicant(s): Kiyoshi Mita
METHOD FOR MANUFACTURING MOUNTING SUBSTRATE
AND METHOD FOR MANUFACTURING CIRCUIT DEVICE

FIG.3



Applicant(s): Kiyoshi Mita
METHOD FOR MANUFACTURING MOUNTING SUBSTRATE
AND METHOD FOR MANUFACTURING CIRCUIT DEVICE

FIG.4A

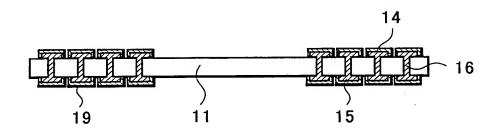


FIG.4B

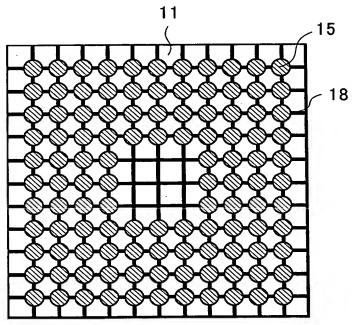


FIG.5

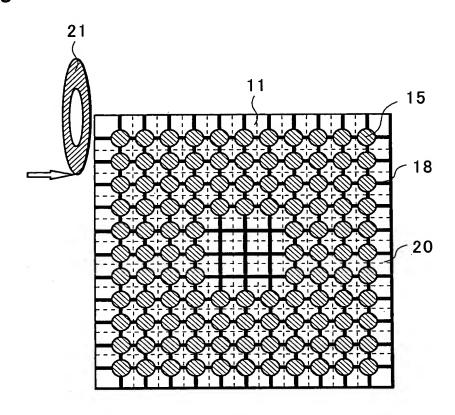
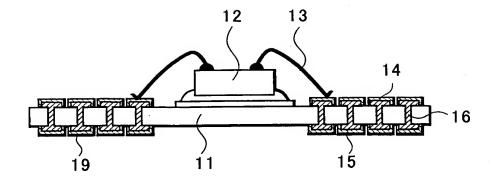


FIG.6



Page 6 of 6

Matter No.: 14225-048001 Page 6 of 6 Applicant(s): Kiyoshi Mita METHOD FOR MANUFACTURING MOUNTING SUBSTRATE AND METHOD FOR MANUFACTURING CIRCUIT DEVICE

FIG.7A

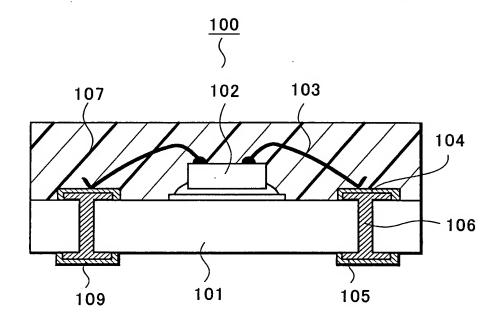


FIG.7B

